UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 7,023,056 B2 APPLICATION NO. : 10/723331

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INVENTOR(S)

DATED

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9, line 24; delete "having au NMOS" insert --having an NMOS--Column 9, line 30; delete "short side a" insert --short side, a--

Signed and Sealed this

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Nineteenth Day of September, 2006

JON W. DUDAS
Director of the United States Patent and Trademark Office